

CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)Applicant(s): **Russell Henry**

Docket No.

03-1094

Serial No.

10/642,954

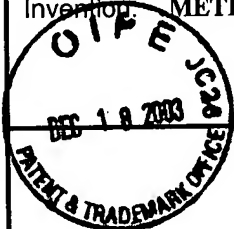
Filing Date

8/18/2003

Examiner

Not Yet Assigned

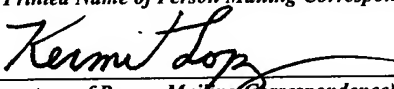
Group Art Unit

1754Invention: **METHODS AND SYSTEMS FOR END-TO-END DATA PROTECTION IN A MEMORY CONTROLLER**I hereby certify that this **REQUEST FOR FILING RECEIPT CORRECTION**
(Identify type of correspondence)

is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on **December 15, 2003**
(Date)**Kermit Lopez**

(Typed or Printed Name of Person Mailing Correspondence)


(Signature of Person Mailing Correspondence)**Note: Each paper must have its own certificate of mailing.**Ortiz & Lopez, PLLC
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Russell Henry
SERIAL NO.: 10/642,954
FILED: 08/18/2003
TITLED: METHODS AND SYSTEMS FOR END-TO-END DATA PROTECTION
IN A MEMORY CONTROLLER

EXAMINER: Unknown
GROUP: 1754
DOCKET NO.: 03-1094

REQUEST FOR FILING RECEIPT CORRECTION

Office of Initial Patent Examination's Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir or Madam:

Please note that an error was identified on the filing receipt for the above-referenced patent application. A copy of the filing receipt and corrections thereof are included herewith. The title of the above-referenced patent application was incorrectly listed on the filing receipt as "Methods and Systems for end-to-and data protection in a memeory controller". Note that the words "end-to-and" and "memeory" were spelled incorrectly on the filing receipt. The title of the above-referenced should be corrected to read as follows:

"Methods and Systems for End-to-End Data Protection in a Memory Controller"

We request that a corrected filing receipt be mailed to the address below:

**LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035**

Please direct any questions to the undersigned at (505) 255-4536.

Respectfully submitted,

Kermit Lopez
Registration No. 41,953



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
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 Alexandria, Virginia 22313-1450
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/642,954	08/18/2003	1754	750	03-1094	4	19	3

CONFIRMATION NO. 4186

Pete Scott
 Senior Corporate Counsel
 Intl. Property Law Dept., LSI Logic Corporation
 Mail Stop D-106, 1551 McCarthy Boulevard
 Milpitas, CA 95035

FILING RECEIPT



OC000000011268866

NOV 17 2003

Date Mailed: 11/14/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Russell Henry, Wichita, KS;

Assignment For Published Patent Application

LSI Logic Corporation.;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 11/13/2003

Projected Publication Date: 02/24/2005

Non-Publication Request: No

Early Publication Request: No

Title

Methods and systems for ~~end-to-end~~ ^{memory} data protection in a ~~memory~~ ^{memory} controller

Preliminary Class
502

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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